

Figure 1

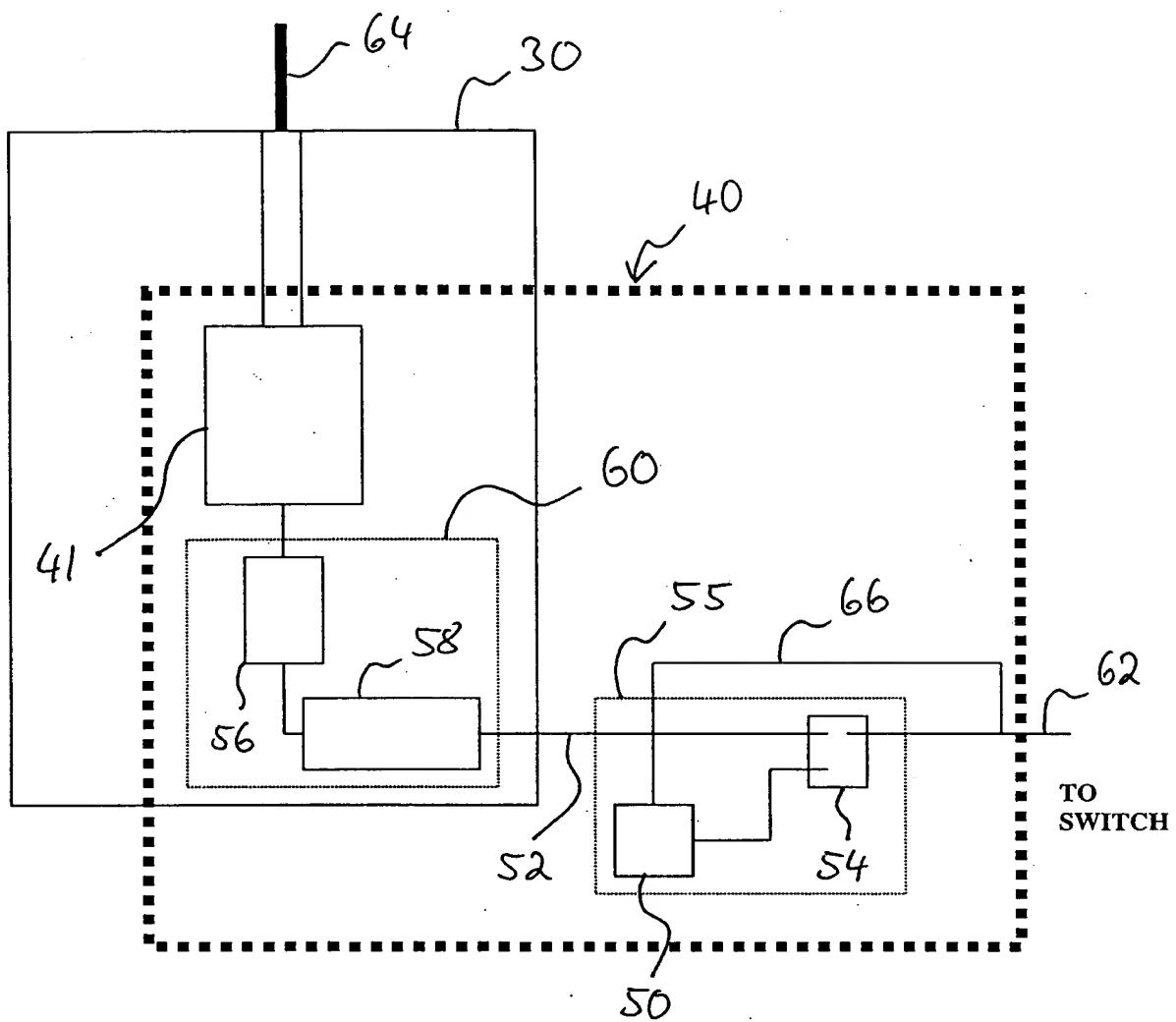


Figure 2

## 30

Bar  
✓

FIG. 3

# Multi-rate Clock / Data Recovery Circuit with 2R Bypass Mode

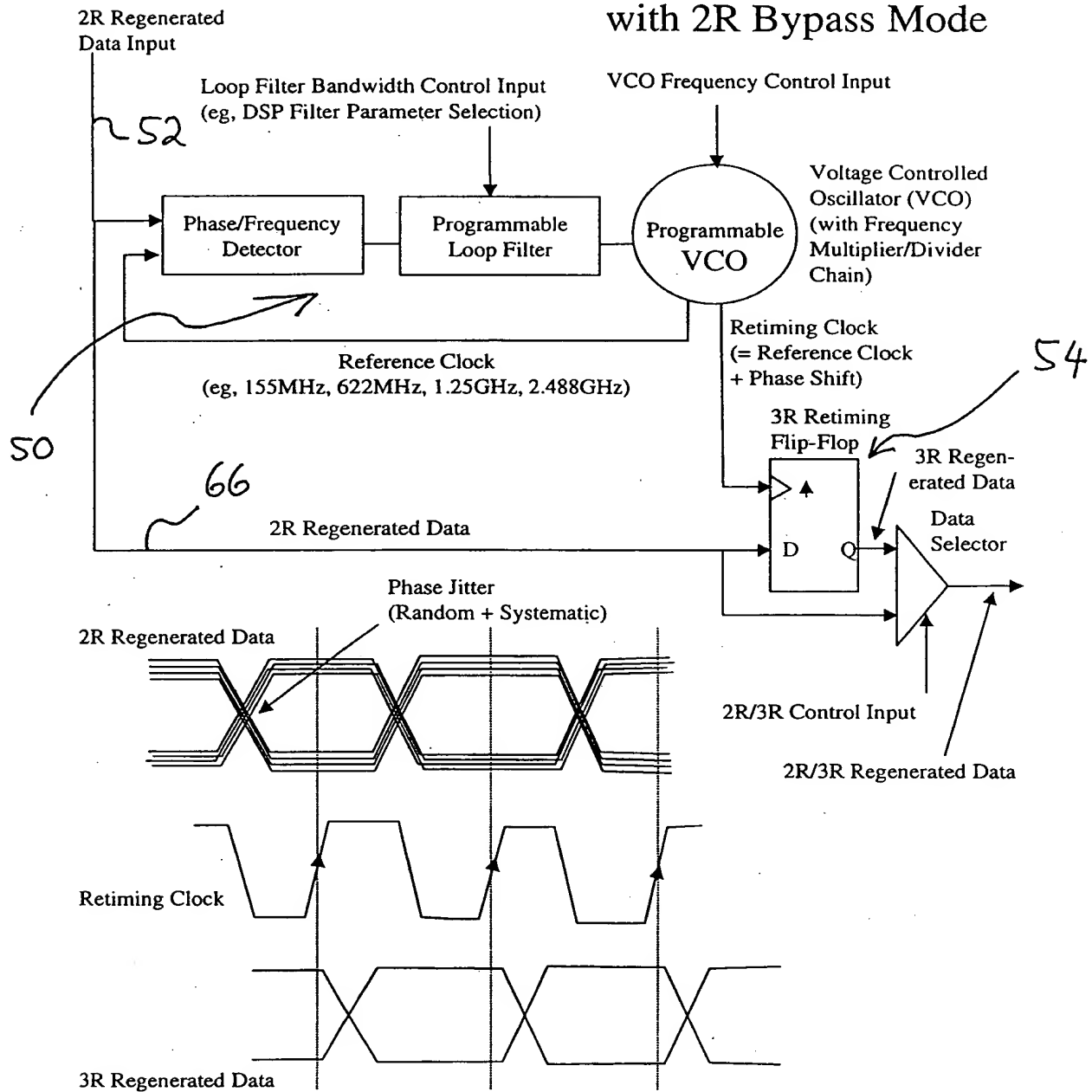


FIG. 4

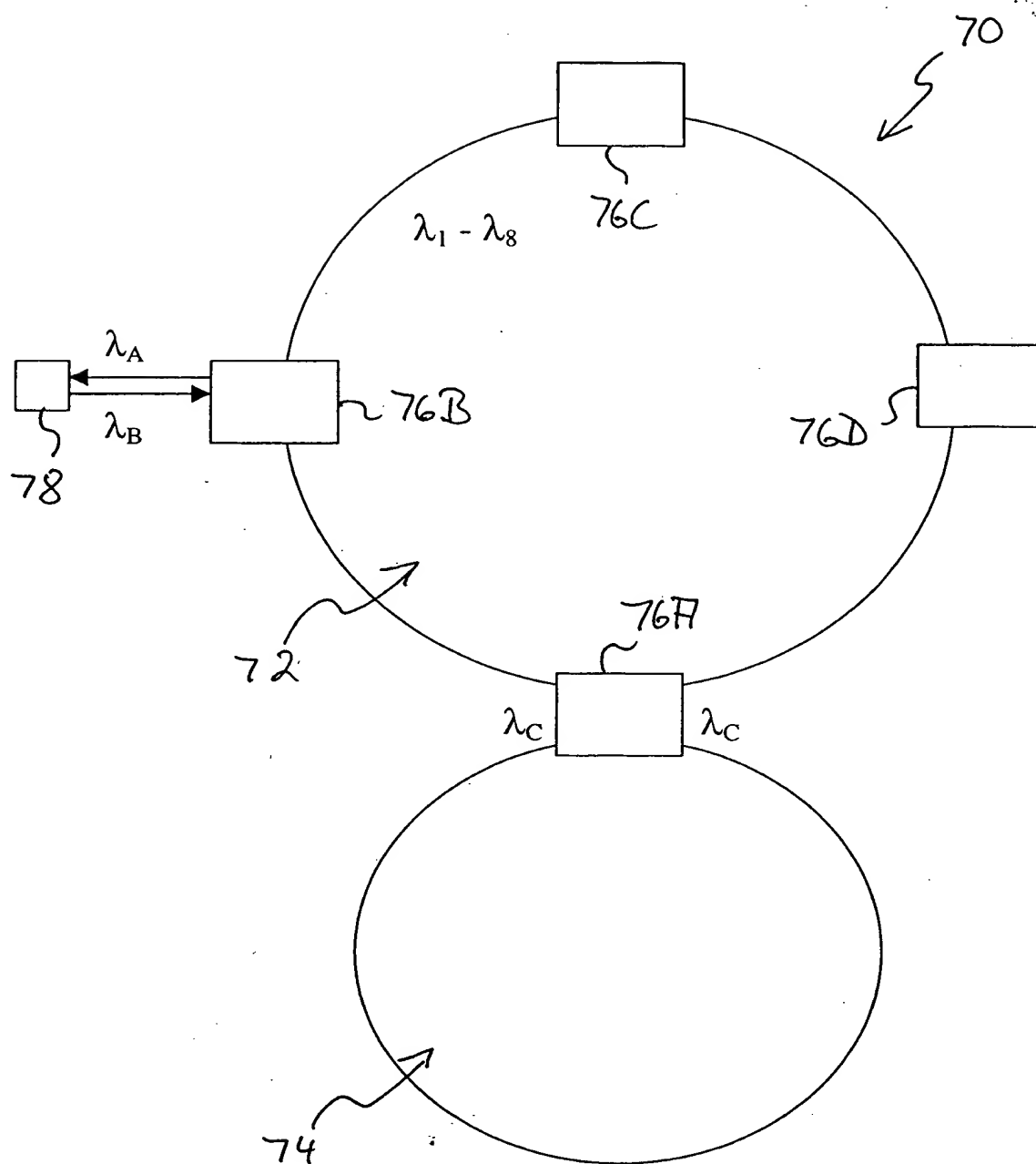


Figure 5